

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	5357	(updat\$ upgrad\$)near5(firmware boot bios)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 14:39
S3	167556	(flash\$ eeprom reprogram\$ programm\$)same(chipset (chip adj set) super (single near4(ic circuit chip device)) bridge controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 16:56
S4	1910	S2 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/13 15:13
S5	332	S2 same S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/13 15:13
S6	128	S5 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:02
S7	66146	(updat\$ upgrad\$ reprogramm\$ replac\$)near5(firmware boot bios program software)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 18:04
S8	63433	(flash\$ eeprom reprogram\$ programm\$)with(single chipset bridge soc (system near chip) controller (integrated adj circuit) ic)with(implement\$ inclu\$4 contain\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:00
S9	595	S7 same S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:02
S10	300	S9 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 14:44
S11	1	"5647986".PN.	USPAT; USOCR	OR	ON	2006/01/17 17:12

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S12	7539	(updat\$ upgrad\$ reprogramm\$ replac\$)near5(firmware boot bios)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:32
S13	1865	(flash\$ eeprom eprom)same S12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:33
S14	52083	(flash\$ eeprom reprogram\$ programm\$ eprom)with(single chipset bridge soc (system near chip))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:01
S15	246	S13 and S14 and((down adj load\$4) download\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 18:05
S16	81	S15 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:50
S17	1107	(flash\$ eeprom)with(single adj2(chip chipset soc (integrated adj circuit) ic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:50
S18	152	S7 and S17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 17:50
S19	97	S18 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 16:40
S20	1613	(updat\$ upgrad\$ reprogramm\$ replac\$)near5(firmware boot bios)same flash\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 18:41
S21	706	S20 and((down adj load\$4) download\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 18:05

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S22	282	S21 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 18:41
S23	4571	(imbed\$ embed\$)near8(nonvolatile (non adj volatile) eeprom eprom reprogramm\$ flash\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 18:40
S24	7539	(updat\$ upgrad\$ reprogramm\$ replac\$)near5(firmware boot bios)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 18:41
S25	105	S23 and S24 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:02
S26	24584	(nonvolatile (non adj volatile) eeprom eprom flash\$)with(single chipset bridge soc (system near chip))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:14
S27	679	S24 and S26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:01
S28	187	(S27 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930"))not(S10 S16 S22 S25)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:18
S29	25259	(nonvolatile (non adj volatile) eeprom eprom flash\$ nvram nvram)with(ic (integrated adj circuit) chip chipset bridge soc controller)with(inclu\$4 implement\$ embed\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:17
S30	15073	(nonvolatile (non adj volatile) eeprom eprom flash\$ nvram nvram)with(bios firmware boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:18

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S31	703	S24 and S29 and S30	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:18
S32	130	(S31 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930"))not(S10 S16 S22 S25 S28)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 19:19
S33	5042	(updat\$ upgrad\$)near4(firmware bios boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:15
S34	15169	(flash\$ eprom eeprom reprogramm\$ nonvolatile (non adj volatile))with(firmware bios boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:46
S35	236	pars\$ and S33 and S34	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 10:17
S36	88	S35 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 11:08
S42	9277	(nonvolatile (non adj volatile) eeprom eprom reprogramm\$ flash\$)near7((chipset soc (system near chip) controller (integrated adj circuit) ic)near5(implement\$ inclu\$4 contain\$ incorpor\$ compris\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 11:14
S43	116	S33 and S34 and S42 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 14:39
S44	1	"5647986".PN.	USPAT; USOCR	OR	ON	2006/01/19 11:23
S45	3482	(updat\$ upgrad\$)near5(flash\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:10

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S46	5438	(flash\$ eeprom nvmem nvram nonvolaile (non adj volatile))with(chipset (chip adj set)(single near2((ic adj circuit) chip device)) bridge ((super multi\$3 io output)near2 controller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:09
S47	282	S45 and S46	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 14:44
S48	145	S47 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:10
S49	21133	(flash\$ eeprom nvmem nvram nonvolaile (non adj volatile))with(internal\$ embed\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:11
S50	7971	(updat\$ upgrad\$)near5(flash\$ bios boot firmware)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:10
S51	567	S49 and S50 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:10
S52	13196	(flash\$ eeprom nvmem nvram nonvolaile (non adj volatile))with(firmware bios boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:11
S53	337	S51 and S52	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 15:12
S54	284	S53 not S48	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 16:39

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S55	2	"5805882".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/19 16:39
S56	2	"5479638".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 14:58
S57	2	"5388083 ".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 14:58
S58	2	"5485595 ".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 14:59
S59	2	"5845313".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 15:01
S60	2	"6182162".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 15:02
S61	2	"6385667".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 15:03
S62	2	"6202138".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 15:38
S63	6769	(upgrad\$ updat\$ replac\$)near4(bios firmware boot (basic adj input adj output))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 16:32
S64	1897	S63 same(flash\$ eeprom e2prom (non adj volatile) nvmem nonvolatile nv)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 17:30

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S65	71779	(controller chipset chip ic (integrated adj circuit) device (input adj output))with(flash\$ eeprom e2prom (non adj volatile) nvmem nonvolatile nv)with(embed\$ implement\$ incorporat\$ includ\$3 built)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 16:40
S66	881	S64 and S65	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 16:40
S67	354	S66 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 17:16
S68	590	S63 with(\$self automatic\$ autonomous\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 17:15
S69	187	S64 and S68	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 17:15
S70	61	S69 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 17:34
S71	3240	(chipset single controller)near7(implement\$ contain\$ embed\$ built incorporat\$ internal\$)near7(flash\$ eeprom e2prom (non adj volatile) nvmem nonvolatile nv)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 17:32
S72	65	S63 and S71 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/23 17:34
S73	6453	(updat\$ upgrad\$ replac\$ download\$ (down adj load\$4))near2(bios firmware boot (basic adj input adj output))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 16:11

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S74	7176	(flash\$ eeprom e2prom erasable nvmem nvram nv (non adj volatile) nonvolatile)near8(chipset (chip adj set) super (single adj2(ic circuit chip device die integr\$)) bridge controller)near7(implement\$ integr\$ incorporat\$ contain\$ imbed\$ embed\$ includ\$3 internal\$ built)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 18:04
S75	29	S73 same S74	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 16:33
S76	302	(S73 and S74)not S75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 16:33
S77	162	S76 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 17:57
S78	15644	(flash\$ eeprom e2prom erasable nvmem nvram nv (non adj volatile) nonvolatile)near2(integr\$ imbed\$ embed\$ internal\$ built soc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 17:16
S79	168	(S73 and S78 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930"))not S77	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 17:17
S80	227	(flash\$ eeprom e2prom)near8(chipset (chip adj set))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 17:56
S81	26	S73 and S80	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 17:49
S82	1268	(flash\$ eeprom e2prom)near2(embed\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 17:56

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S83	78	S73 and S82	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 17:56
S84	11	S83 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 18:05
S85	1685	(flash\$ eeprom e2prom erasable nvram nv (non adj volatile) nonvolatile)near8(single adj2(ic circuit chip device die integr\$))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 18:04
S86	15	S73 and S85 and(@ad<"19990930" @prad<"19990930" @rlad<"19990930")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/24 18:45